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"instruction scheduling" priority "execution timing"

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Assume that low **priority** task B is running and triggers higher **priority** task A. Since task ...  
no **instruction scheduling** optimizations ...

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work on coarse-grained **instruction scheduling** and execu- tion [27, 28] and a dynamic  
binary ... cycle micro-ops are given higher **priority** as the head of a ...

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Performing coarse-grained **instruction scheduling** on groups of multiple instruc- ... the  
**execution timing** of each instruction can be inferred several clock ...

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Static **priority** scheduling (eg RMS) or dynamic scheduling (eg EDF) are hard to analyze ...  
They apply dynamic **instruction scheduling** leading to out-of-order ...

[www.ida-ing.tu-bs.de/research/publications/ps/YE96:EmbedProgrTiminAnaly.ps](http://www.ida-ing.tu-bs.de/research/publications/ps/YE96:EmbedProgrTiminAnaly.ps) -

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Instruction **Execution Timing**. The 2-stage pipeline of the Functional Unit ... Assign a Rank  
(**priority**) to each instruction (or node). ...

[www.cs.nyu.edu/courses/spring01/G22.3033-005/instruction\\_scheduling\\_palem\\_wong.ppt](http://www.cs.nyu.edu/courses/spring01/G22.3033-005/instruction_scheduling_palem_wong.ppt)

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ing with an energy-centric **priority** function. The algorithm, entitled ... all reachable tasks  
finish their **execution**. **Timing** constraints resulting from ...

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[High-performance, superscalar-based computer system with out-of ...](#)

Lam, MS, "[Instruction Scheduling For Superscalar Architectures](#)," Annu. ... Serviced in  
**priority** order between atomic (ordinary) instructions and may suspend ...

[www.freepatentsonline.com/6941447.html](http://www.freepatentsonline.com/6941447.html) - 257k - [Cached](#) - [Similar pages](#)

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ffl The **execution timing** of DSP code is very accurately predictable. ... ffl static, **priority**-  
driven scheduling, ffl synchronization with semaphores, ...

[os.inf.tu-dresden.de/papers\\_ps/baumgartl-phd.ps](http://os.inf.tu-dresden.de/papers_ps/baumgartl-phd.ps) - [Similar pages](#)

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**Instruction scheduling** by compiler. Mandatory for issuing independent operations in SS;  
Increasingly important as ... Another Example – **Execution Timing** ...

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**instruction scheduling.** As a matter of fact, it is hard to define exact instruction ...  
dependency, partially define the **execution timing** of a processor. ...

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